

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Confirmation No.: 3838
Hiroki Goko)	
Application No. 10/813,031)	Examiner: MEMULA, Suresh
Filed: March 31, 2004)	Group Art Unit: 2825
For: APPARATUS AND METHOD FOR DESIGNING)	
SEMICONDUCTOR INTEGRATED CIRCUIT)	Date: June 15, 2007

AMENDMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action mailed August 15, 2006, please amend the above-identified application as follows: